

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A method for producing a device having a reduced memory capacity comprising:
 providing a device having a memory capacity;
 determining that the memory capacity of the device can be reduced;
 determining an amount by which the memory capacity of the device is to be reduced; and
 reducing the memory capacity of the device in accordance with the determined amount to produce a device having a reduced memory capacity and a dormant memory capacity, wherein the memory capacity is reduced by inactivating at least one row of a memory bank of the device or at least one column of the memory bank of the device.

Claim 2 (currently amended): The method of Claim 1 additionally comprising using the device in an apparatus ~~determining a second memory capacity of a second device in an apparatus requiring the second device having the second memory capacity.~~

Claim 3 (original): The method of Claim 2 wherein said apparatus comprises a computer.

Claim 4 (cancelled)

Claim 5 (cancelled)

Claim 6 (currently amended): The method of Claim 2, ~~additionally comprising wherein using the device in the~~ apparatus comprises furnishing said apparatus with said device having said reduced memory capacity and the dormant memory capacity which is not available for use.

Claim 7 (original): The method of Claim 3 additionally comprising furnishing said apparatus with said device having said reduced memory capacity and the dormant memory capacity which is not available for use.

Claim 8 (cancelled)

Claim 9 (cancelled)

Claim 10 (cancelled)

Claim 11 (cancelled)

Claim 12 (original): The method of Claim 1 wherein said device comprises a module.

Claim 13 (original): The method of Claim 3 wherein said device comprises a module.

Claim 14 (cancelled)

Claim 15 (cancelled)

Claims 16-28 (previously cancelled)

Claim 29 (original): A device produced in accordance with the method of Claim 1.

Claim 30 (previously cancelled)

Claim 31 (currently amended): An assembly comprising an apparatus; and a device disposed in said apparatus and having a reduced memory capacity and a dormant memory capacity, wherein a memory capacity of the device is reduced in accordance with a determined amount in order to achieve the reduced memory capacity, wherein the memory capacity is reduced by inactivating at least one row of a memory bank of the device or at least one column of the memory bank of the device.

Claim 32 (original): The assembly of Claim 31 wherein said device having been produced in accordance with the following steps:

- providing the device having a memory capacity;
- determining that the memory capacity of the device can be reduced;
- determining an amount by which the memory capacity of the device is to be reduced; and

reducing the memory capacity of the device in accordance with the determined amount to produce the device having the reduced memory capacity and the dormant memory capacity.

Claim 33 (currently amended): A computer assembly comprising a computer; and a dual inline memory module (DIMM) disposed in said computer and having a reduced memory capacity and a dormant memory capacity, wherein a memory capacity of the DIMM is reduced in accordance with a determined amount in order to achieve the reduced memory capacity, wherein the memory capacity is reduced by inactivating at least one row of a memory bank of the DIMM or at least one column of the memory bank of the DIMM.

Claim 34 (original): The computer assembly of Claim 33 wherein said DIMM having been produced in accordance with the following steps:

- providing the DIMM having a memory capacity;
- determining that the memory capacity of the DIMM can be reduced;
- determining an amount by which the memory capacity of the DIMM is to be reduced; and
- reducing the memory capacity of the DIMM in accordance with the determined amount to produce the DIMM having the reduced memory capacity and the dormant memory capacity.

Claim 35 (original): The method of Claim 1 additionally comprising testing the produced device for a reduced

memory-capacity accuracy and for a dormant memory-capacity accuracy.

Claim 36 (currently amended): A method for producing a device having a reduced memory capacity, the method comprising:

providing a device having a memory capacity;

determining an amount of reduction for the memory capacity of the device; and

reducing the memory capacity of the device by the amount of reduction in order to produce a device having a reduced memory capacity and a dormant memory capacity, wherein the memory capacity is reduced by inactivating at least one row of a memory bank of the device or at least one column of the memory bank of the device.

Claim 37 (previously presented): The method of claim 36, wherein the device comprises a DIMM.

Claim 38 (previously presented): The method of claim 36, wherein reducing the memory capacity of the device comprises:

making at least one row of a memory bank in the device as dormant.

Claim 39 (previously presented): The method of claim 36, wherein reducing the memory capacity of the device comprises:

making at least one column of a memory bank in the device as dormant.

Claim 40 (previously presented): The method of claim 36, wherein reducing the memory capacity of the device comprises:

making at least one memory bank of the device as dormant.

Claim 41 (previously presented): The method of claim 36, further comprising:

testing the device for memory-reduction accuracy to ensure that a desired amount of memory capacity was reduced in the device.

Claim 42 (previously presented): A device produced in accordance with the method of claim 36.

Claim 43 (currently amended): An apparatus comprising:

a device having a reduced memory capacity and a dormant memory capacity, wherein a memory capacity of the device is reduced in accordance with a determined amount in order to achieve the reduced memory capacity, wherein the memory capacity is reduced by inactivating at least one row of a memory bank of the device or at least one column of the memory bank of the device.

Claim 44 (previously presented): The apparatus of claim 43, wherein the device comprises a DIMM.

Claim 45 (previously presented): The apparatus of claim 43, wherein the device is produced in accordance with a method comprising:

providing the device having a memory capacity; and
reducing the memory capacity of the device by an amount of reduction in order to produce a device having a reduced memory capacity and a dormant memory capacity.

Claim 46 (previously presented): The apparatus of claim 45, wherein reducing the memory capacity of the device comprises:

making at least one row of a memory bank in the device as dormant.

Claim 47 (previously presented): The apparatus of claim 45, wherein reducing the memory capacity of the device comprises:

making at least one column of a memory bank in the device as dormant.

Claim 48 (previously presented): The apparatus of claim 45, wherein reducing the memory capacity of the device comprises:

making at least one memory bank of the device as dormant.

Claim 49 (previously presented): The apparatus of claim 45, wherein the method further comprises:

testing the device for memory-reduction accuracy to ensure that a desired amount of memory capacity was reduced in the device.

Claim 50 (previously presented): The assembly of claim 31, wherein a determination is made if the memory capacity of the device can be reduced prior to determination of the determined amount.

Claim 51 (previously presented): The assembly of claim 33, wherein a determination is made if the memory capacity of the DIMM can be reduced prior to determination of the determined amount.

Claim 52 (previously presented): The method of claim 36, further comprising:

prior to determining the amount of reduction, determining if the memory capacity of the device can be reduced.

Claim 53 (previously presented): The apparatus of claim 43, wherein a determination is made if the memory capacity of the device can be reduced.

Claim 54 (currently amended): An assembly comprising an apparatus; and a device disposed in said apparatus and having a reduced memory capacity and a dormant memory capacity;

wherein said device having been produced in accordance with the following steps:

providing the device having a memory capacity;
determining that the memory capacity of the device
can be reduced;
determining an amount by which the memory capacity of
the device is to be reduced; and
reducing the memory capacity of the device in
accordance with the determined amount to produce the device
having the reduced memory capacity and the dormant memory
capacity, wherein the memory capacity is reduced by
inactivating at least one row of a memory bank of the
device or at least one column of the memory bank of the
device.

Claim 55 (currently amended): A computer assembly
comprising a computer; and a dual inline memory module
(DIMM) disposed in said computer and having a reduced
memory capacity and a dormant memory capacity;

wherein said DIMM having been produced in accordance
with the following steps:

providing the DIMM having a memory capacity;
determining that the memory capacity of the DIMM can
be reduced;
determining an amount by which the memory capacity of
the DIMM is to be reduced; and
reducing the memory capacity of the DIMM in accordance
with the determined amount to produce the DIMM having the
reduced memory capacity and the dormant memory capacity,
wherein the memory capacity is reduced by inactivating at
least one row of a memory bank of the DIMM or at least one
column of the memory bank of the DIMM.

Claim 56 (new): The method of claim 1, wherein inactivating the at least one row or at least one column comprises:

making the at least one row or at least one column as dormant.

Claim 57 (new): The method of claim 1, wherein inactivating the at least one row or at least one column comprises:

making the at least one row or at least one column as unaddressable.

Claim 58 (new): The method of claim 1, wherein the reduced memory capacity is equal to a memory capacity of another device that is to be replaced in a computer.

Claim 59 (new): The assembly of claim 31, wherein inactivating the at least one row or at least one column comprises:

making the at least one row or at least one column as dormant.

Claim 60 (new): The assembly of claim 31, wherein inactivating the at least one row or at least one column comprises:

making the at least one row or at least one column as unaddressable.

Claim 61 (new): The assembly of claim 31, wherein the reduced memory capacity is equal to a memory capacity of another device that is to be replaced in the apparatus.

Claim 62 (new): The computer assembly of claim 33, wherein inactivating the at least one row or at least one column comprises:

making the at least one row or at least one column as dormant.

Claim 63 (new): The computer assembly of claim 33, wherein inactivating the at least one row or at least one column comprises:

making the at least one row or at least one column as unaddressable.

Claim 64 (new): The computer assembly of claim 33, wherein the reduced memory capacity is equal to a memory capacity of another DIMM that is to be replaced in the computer.

Claim 65 (new): The method of claim 36, wherein inactivating the at least one row or at least one column comprises:

making the at least one row or at least one column as dormant.

Claim 66 (new): The method of claim 36, wherein inactivating the at least one row or at least one column comprises:

making the at least one row or at least one column as unaddressable.

Claim 67 (new): The method of claim 36, wherein the reduced memory capacity is equal to a memory capacity of another device that is to be replaced in a computer.

Claim 68 (new): The apparatus of claim 43, wherein inactivating the at least one row or at least one column comprises:

making the at least one row or at least one column as dormant.

Claim 69 (new): The apparatus of claim 43, wherein inactivating the at least one row or at least one column comprises:

making the at least one row or at least one column as unaddressable.

Claim 70 (new): The apparatus of claim 43, wherein the reduced memory capacity is equal to a memory capacity of another device that is to be replaced in a computer.